



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): KIMOTO, Hisamitsu

Serial No.: 09/850,254

Filed: May 7, 2001

Title: SEMICONDUCTOR APPARATUS
INCLUDING A MULTI-LAYER WIRING
CONFIGURATION AND
MANUFACTURING METHOD THEREFOR

Attorney Docket No.: N13476100S

Group Art Unit: 2826

Examiner: Quinto, K.

(DE)
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J
6/6/B

RESPONSE TO FINAL OFFICE ACTION

5 Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

10 A. Introductory Comments

The following is submitted in response to the FINAL Office Action dated March 26, 2003. The two month Advisory Action period ends May 26, 2003.

15 This response is presented in the revised amendment format set forth in 1265 Off. Gaz. Pat. Office 87 (Dec. 17, 2002) and so is subject to waiver of the provisions of 37 C.F.R. §1.21(a), (b) and (c).

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25 I hereby certify that this correspondence is being

37 C.F.R. §1.8

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[X] deposited with the United States Postal Service as first class mail in an envelope addressed to: Mail Stop AF, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313.

35 Date of Deposit: May 23, 2003

Typed/Printed Name: Darryl Walker

40 Signature: [Signature]